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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/645,024	08/21/2003	James Allan Kahle	AUS920030139US1	7352
7590	03/09/2006		EXAMINER	
Gregory W. Carr 670 Founders Square 900 Jackson Street Dallas, TX 75202				BUTLER, DENNIS
		ART UNIT		PAPER NUMBER
		2115		

DATE MAILED: 03/09/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/645,024	KAHLE ET AL.	
	Examiner Dennis M. Butler	Art Unit 2115	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 1 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 21 August 2003.

2a) This action is FINAL. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-15 is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) _____ is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) 1-15 are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some * c) None of:

1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date: _____
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date: _____	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
	6) <input type="checkbox"/> Other: _____

Art Unit: 2115

1. This action is in response to the application filed on August 21, 2003. Claims 1-15 are pending.
2. Restriction to one of the following inventions is required under 35 U.S.C. 121:
 - I. Claims 1, 3 and 4, drawn to supplying voltage and clocks to only a section or portion of a bit data register, classified in class 713, subclass 322.
 - II. Claims 2 and 13, drawn to enabling or disabling a function of a microprocessor using a machine state register, classified in class 713, subclass 1.
 - III. Claim 5, drawn to providing voltage to sections of data flow circuitry, classified in class 713, subclass 324.
 - IV. Claims 6, 9 and 14, drawn to activating sectional portions of data transfer registers, classified in class 713, subclass 323.
 - V. Claims 7, 9 and 15, drawn to activating specialized computational portions of a CPU, classified in class 713, subclass 323.
 - VI. Claims 8 and 9, drawn to activating portions of a clock signal distribution system, classified in class 713, subclass 323.
 - VII. Claims 10, 11 and 12, drawn to reconfiguring dataflow registers in a configurable CPU, classified in class 713, subclass 100.
3. The inventions are distinct, each from the other because:

Inventions I through VII are related as subcombinations disclosed as usable together in a single combination. The subcombinations are distinct from each other if they are shown to be separately usable. In the instant case, invention I has separate

utility such as supplying voltage and clocks to only a section or portion of a bit data register in order to minimize both the voltage and clock use of the data register.

Invention II has separate utility such as enabling or disabling a function of a microprocessor using a machine state register in order to configure the functionality of the microprocessor. Invention III has separate utility such as providing voltage to sections of data flow circuitry in order to allow parts of the data flow circuitry to be shutdown. Invention IV has separate utility such as activating sectional portions of data transfer registers in order to reduce the power consumption of the data transfer registers. Invention V has separate utility such as activating specialized computational portions of a CPU in order to reduce the power consumption of the specialized computational sections. Invention VI has separate utility such as activating portions of a clock signal distribution system in order to reduce the power consumption of the clock distribution system. Invention VII has separate utility such as reconfiguring dataflow registers in a configurable CPU in order to match the software executed by the CPU.

See MPEP § 806.05(d).

4. Because these inventions are distinct for the reasons given above, have acquired a separate status in the art as shown by their different classification and/or by their recognized divergent subject matter and because a different search is required for each Group, restriction for examination purposes as indicated is proper.

5. Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

6. Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a request under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(i).

7. Applicant is advised that the shortened statutory period for this restriction is one month.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dennis M. Butler whose telephone number is 571-272-3663.

The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Dennis M. Butler
Dennis M. Butler
Primary Examiner
Art Unit 2115